



Attorney Docket No. YOR920030306US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): M. Kapur et al.
Docket No.: YOR920030306US1
Serial No.: 10/650,222
Filing Date: August 28, 2003
Group: To Be Assigned
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Lisa L. Vulpis

Date: November 7, 2003

Title: Self-Synchronizing Pseudorandom
Bit Sequence Checker

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed, other than published U.S. patent documents.

U.S. Patent Documents

1. U.S. Patent No. 5,392,289 issued on 02/21/95 to Varian
2. U.S. Patent No. 5,321,754 issued on 06/14/94 to Fisher et al.
3. U.S. Patent No. 5,283,831 issued on 02/01/94 to Cook et al.
4. U.S. Patent No. 4,771,463 issued on 09/13/88 to Beeman
5. U.S. Patent No. 4,639,548 issued on 01/27/87 to Oshima et al.
6. U.S. Patent No. 3,694,757 issued on 09/26/72 to Hanna, Jr.
7. U.S. Patent No. 3,648,237 issued on 03/07/72 to Frey, Jr. et al.

Foreign Documents

1. Great Britain Patent Application 1 281 390 published 07/12/72

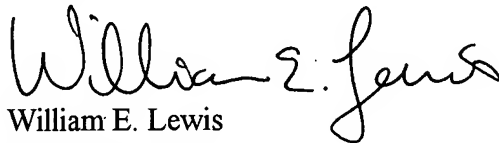
Other Documents

1. Oliver Kromat et al., "A 10-Gb/s Silicon Bipolar IC for PRBS Testing," IEEE Journal of Solid-State Circuits, Vol. 33, No. 1, pp. 76-85, January 1998.

It is believed that there is no fee due in conjunction with the filing of this Information Disclosure Statement. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **International Business Machines Corporation Deposit Account No. 50-0510** as required to correct the error.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,



William E. Lewis
Reg. No. 39,274
Attorney for Applicant(s)
Ryan, Mason & Lewis, LLP
90 Forest Avenue
Locust Valley, NY 11560
(516) 759-2946

Date: November 7, 2003

FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

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U.S. PATENT DOCUMENTS

EXAMINER					FILING DATE	
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE	
___	1. 5,392,289	02/21/95	Varian			
___	2. 5,321,754	06/14/94	Fisher et al.			
___	3. 5,283,831	02/01/94	Cook et al.			
___	4. 4,771,463	09/13/88	Beeman			
___	5. 4,639,548	01/27/87	Oshima et al.			
___	6. 3,694,757	09/26/72	Hanna, Jr.			
___	7. 3,648,237	03/07/72	Frey, Jr. et al.			

FOREIGN PATENT DOCUMENTS

EXAMINER					TRANSLATION	
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES	NO
___	1. GB 1 281 390	07/12/72	Great Britain			

OTHER DOCUMENTS

EXAMINER										
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.								
___	1.	Oliver Kromat et al., "A 10-Gb/s Silicon Bipolar IC for PRBS Testing," IEEE Journal of Solid-State Circuits, Vol. 33, No. 1, pp. 76-85, January 1998.								

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.